

SYMBIOSIS INTERNATIONAL UNIVERSITY



(Established under Section 3 of the UGC Act, 1956 vide notification No. F.9-12/2001-U.3 of the Government of India)
Accredited by NAAC with 'A' grade

Name of the Institute: SIT

Batch: 2011-15

Programme Name: B.Tech.

Semester: IV

Programme Code: 070121

Course Name: Microprocessor Techniques (CS)

Maximum Marks: 45

Course Code: 0701211402

Time: 10.00 AM to 11.30 AM

Date: 19/11/2013

Day: Tuesday

Instructions: Draw diagrams wherever necessary.
All questions are compulsory.

PART-A

Q1

- | | | |
|---|--|---|
| a | What is instruction? How it is classified on the basis of word size? Explain with example. | 2 |
| b | Explain various flags of microprocessor? | 2 |
| c | What do you mean by mnemonics:
i) LDAX Rp ii) ORA M | 2 |
| d | Write any two machine control instructions? | 2 |
| e | In how many groups memory can be classified? Give example of each. | 2 |
| f | What do you mean by tri-state-logic? | 2 |
| g | What are the merits and demerits of I/O mapped I/O? | 2 |
| h | Draw flow chart of interrupt-driven data transfer. | 2 |
| i | How I/O interfacing is different from memory interfacing? | 2 |
| j | Draw 3-to-8 decoder and Dual 2-to-4 decoder. | 2 |

Q2

Solve any 3 Questions

- | | | |
|---|---|---|
| a | Describe stepwise Interrupt process in detail. | 5 |
| b | What are the basic memory interfacing concepts? | 5 |

c Describe 8253 programmable interval timer with neat and clean block diagram. 5

d Describe 8257 DMA controller? 5

Q3 Explain 8085 architecture and its functional blocks in detail. 10